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	Class	Subclass

PATENT NUMBER

U.S. UTILITY Patent Application

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J(8)j

S.I.P.E.

PATENT DATE

APPLICATION NO. 09/675220	CONT/PRIOR F	CLASS 438	SUBCLASS 6	ART UNIT 2812	EXAMINER Eunhwan
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Minoru Nakano
Masaaki Ueno
Kazuo Tanaka

Method and apparatus for controlling a semiconductor fabrication temperature

PTO-2040
12/99

ISSUING CLASSIFICATION

TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
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	_____ (Primary Examiner) _____ (Date)			Amount Due	Date Paid
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.				ISSUE BATCH NUMBER	
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